

16. (Amended) [Viewing screen] A display device according to claim 14
[characterized in that it includes one or more shift registers according to claim 1 as
well as the supplementary lines and columns according to claim 14] further
comprising, a shift register coupled to said selection lines, said shift register
containing a plurality of cascaded stages, a given stage being responsive to two clock
signals, said given stage having an output and an input, said input being coupled to
an output of a preceding stage and to an output of a next stage, said given stage
including,

a first semiconductor output device switching the output of said given stage
between high and low values of a first clock signal, the first semiconductor device
being controlled by a potential of a first node connected:

to the output of the preceding stage via a second semiconductor device
controlled by the output of the preceding stage,

to a negative potential via a third semiconductor device controlled by the
output of the next stage, to a second clock signal via a first capacitance
and to the output of the given stage via a second capacitance.


REMARKS

Claims 1-13 have been cancelled, Claims 14-16 have been reinstated and amended.

Reinstated Claims 14, 15 and 16 are the non-elected claims in the parent patent application.

Allowance of Claims 14-16 is, respectfully, requested.

Respectfully submitted,
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